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ELECTRONIC RESOURCE

2005

Design, implementation, and testing of a software interface between the AN/SPS-65(V)1 radar and the SRC-6E reconfigurable computer / Thomas G. Guthrie
Guthrie, Thomas G.

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#2

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2003

Benchmarking and analysis of the SRC-6E reconfigurable computing system / Kendrick R. Macklin
Macklin, Kendrick R.

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2003

VHDL modeling and simulation of a digital image synthesizer for countering ISAR / Ozkan Kantemir
Kantemir, Ozkan.

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2003

Design, construction and testing of a reduced-scale cascaded multi-level converter / Robert A. Crowe
Crowe, Robert A.

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#5

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2002

Performance analysis of a digital image synthesizer as a counter-measure against inverse synthetic aperture radar / by Fernando A. Le Dantec
Le Dantec, Fernando A.

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#6

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2002

VHDL modeling and simulation for a digital target imaging architecture for multiple large targets generation / by Håkan Bergön
Bergön, Håkan.

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#7

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2002

Design, implementation, and testing of a VLSI high performance ASIC for extracting the phase of a complex signal / by Ronald Christopher Altmeyer
Altmeyer, Ronald Christopher.

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#8

TK5105.59 .S96 2002

2002

Symposium 2002 presentations / CINFINIS *Revised ed.*
Naval Postgraduate School (U.S.). Cebrowski Institute for Information Innovation and Superiority.

► [URL](#)

#9

A438546

2001

Design, implementation, and testing of a high performance summation adder for radar image synthesis
Amundson, Craig A.

► [URL](#)

#10

C19185

2001

Design of a synchronous pipelined multiplier and analysis of clock skew in high-speed digital systems / John R. Clavert, Jr
Clavert, John R.

► [URL](#)

#11

S437364

2000

Design and performance analysis of an asynchronous pipelined multiplier with comparison to synchronous implementation
Shawhan, Kirk A.

► [URL](#)

#12

H103455

2000

Design of a low power embedded microprocessor for a hands-eyes-ears-free personal navigation and communications system
Haase, Peter H.

► [URL](#)

#13
W82177

1999

Miniaturization of a microcontroller for the tactile situational awareness system

Wood, Terrence L.

[▶ URL](#)

#14
L66346

1999

Design of a serial communication protocol and bus interface chip for tactile communications

Link, Jeffrey P.

[▶ URL](#)

#15
L89295

1998

Design of a microelectronic controller with a MIL-STD-1553 bus interface for the tactile situation awareness system

Luke, Brian L.

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#16
C44936

1998

Architectural development and performance analysis of a primary data cache with read miss address prediction capability

Christensen, Kathryn S.

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#17
N65815

1997

Alternative gate designs for improved radiation hardness in bulk CMOS integrated circuits

Noe, Sidney Scott.

#18
B24256175

1997

Design of an 8 x 8 non-blocking crosspoint switch in GaAs two-phase dynamic FET logic

Bates, David R.

[▶ URL](#)

#19
T43526

1997

Design, construction and programming of a microcontroller-based testbench suitable for radiation testing of microelectronic circuits

Thompson, John A.

#20
A4385426

1996

Design of a universal test platform for radiation testing of digital components / Duane E. Amsler, Jr

Amsler, Duane E.

#21
A4275

1996

Development of a new prediction algorithm and a simulator for the Predictive Read Cache (PRC)

Altmisdort, F. Nadir.

#22
C19313

1996

Simulation and analysis of Predictive Read Cache (PRC) performance

Camligüney, Altay.

#23
H8415

1996

Speed, power consumption, and impedance in gallium arsenide IC interconnection circuits / Hugh J. Huck III

Huck, Hugh Joseph.

#24
S4387

1996

Low-power high-speed dynamic logic families for complementary gallium arsenide (CGaAs) fabrication processes

Shehata, Khaled Ali.

#25
M742

1996

Design of a satellite-based microelectronic radiation testing experiment

Mooney, Christopher S.

#26
R5925

1996

The design of a predictive read cache / Joseph R. Robert, Jr
Robert, Joseph R.

#27
A494

1995

Gallium arsenide DRAM memory cell design and evaluation of test methods / Peter A. Andreasen
Andreasen, Peter Allen.

#28
M2748633

1995

Design techniques for the prevention of radiation-induced latch-up in bulk CMOS processes
Madsen, Anne.

[▶ URL](#)

#29
B242344

1995

A CMOS current-mode full-adder cell for multi-valued logic VLSI / Robert James Barton III
Barton, Robert James.

#30
V16323

1995

Single event upsets and noise margin enhancement of gallium arsenide Pseudo-Complimentary MESFET Logic
Van Dyk, Steven E.

[▶ URL](#)

#31
M58856

1995

Simulation and analysis of predictive read cache performance
Miller, Robert W.

#32
A2677

1995

Testing of a read prediction buffer integrated circuit and design of a predictive read cache
Aguilar F., Max E.

[▶ URL](#)

#33
R329717 **1995**
Testing of a CMOS VLSI IC for real-time opto-electronic two-dimensional histogram generation
Reinagel, Peter J.

#34
G25757 **1995**
Design of a VLSI charge-coupled device analog delay line
Gedra, David R.

#35
J2183 **1995**
A CMOS, VLSI, implementation of a FFT for cyclic spectral analysis
Jackson, Kevin L.

#36
S2466 **1995**
Decimation of encoding errors in an optimum SNS 2[μ] low-noise CMOS ADC
Schafer, Jeffrey L.

#37
G4526 **1994**
Design, fabrication, and assembly of a test platform for a high-speed GaAs DRAM VLSI IC / by Byron A. Ginter II
Ginter, Byron A. [▶ URL](#)

#38
C27143 **1994**
Analog preprocessing in a SNS 2[μ] low-noise CMOS folding ADC
Carr, Richard D. [▶ URL](#)

#39
S6058 **1994**
SACS : a cache simulator incorporating timing analysis with buffer and memory management
Smith, William G.

#40
L554

1994

A platform independent application of Lux illumination prediction algorithms

Lester, Michael Theodore.

1994

Preliminary flight software specification for the Petite Amateur Navy Satellite (PANSAT)

Ford, Teresa Owen.

#42
D537

1994

A VLSI interface for the NM24CF04 serial-access ferroelectric memory

Dickerson, James H.

#43
W69355

1993

Radiation tolerant, high speed, low power gallium arsenide logic

Wolfe, Kurt A.

#44
R3955

1993

A CMOS VLSI IC for real-time opto-electronic two-dimensional histogram generation

Richstein, James K.

#45
H8478

1993

Gallium arsenide MESFET operational amplifier to be used in composite operational amplifier design

Hudson, Benjamin Lenward.

#46
E6863

1993

An analog preprocessing architecture for high-speed analog-to-digital conversion

Esparza, Jorge A.

#47
B9463

1993

Test methods and custom hardware for functional testing of a high speed GaAs DRAM

Butler, Michael P.

#48
P6724

1993

Microprocessor controlled instrumentation/navigation package for hostile marine environments

Poitras, Robert Lee.

#49
M82373

1993

Gallium arsenide dynamic random access memory support circuitry

Morris, Michael Andrew.

#50
F66465

1993

Networked workstations and parallel processing utilizing functional languages / by Stanley L. Fox II

Fox, Stanley L.

#51
N9457

1992

The design and implementation of a read prediction buffer

Nowicki, Gary Joseph.

#52
B5417

1992

An investigation of memory latency reduction using an address prediction buffer / by Arthur B. Billingsley, Jr

Billingsley, Arthur Brooks.

#53
V1236

1992

A single-transistor memory cell and sense amplifier for a gallium arsenide dynamic random access memory

Vagts, Christopher Bryan.

#54
G54753

1991

A microprocessor interface for the NM24CF04 serial-access ferroelectric memory

Gonter, Thomas C.